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3/12/03

DOCKET NO: 99-TK-552SS  
Express Mail Label No.: EV 208497520 US  
Date of Deposit: January 31, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: David Alan Edwards  
Serial No: 09/410,606  
Confirmation No: 7114  
Filed: October 1, 1999  
For: APPARATUS AND METHOD FOR SHADOWING  
PROCESSOR INFORMATION

Examiner: Michael C. Maskulinski  
Art Unit: 2184

BOX CPA  
Commissioner for Patents  
Washington, D.C. 20231

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FEB 04 2003  
Technology Center 2100

PRELIMINARY AMENDMENT

Sir:

Applicant requests that the following amendments be made to the above application prior to Examination on the merits:

In the Claims:

Please amend the claims as follows.

Applicant presents the claims as amended below and encloses a separate sheet indicating the amendments to the claims with bracketing and underlining.

- SubC 101  
BT
1. (Amended) A microcomputer comprising:  
at least one processor;  
a debug circuit, wherein the processor and debug circuit are implemented on a same integrated circuit;  
a system bus coupling the processor and debug circuit; and